

## ABSTRACT OF THE DISCLOSURE

A sequential comparison type AD converter comprising series resistors for generating at respective connection portions reference values to convert an analog value to an m-bit digital value; a comparator for sequentially comparing the analog value and one of the reference value and outputting a digital value; a plurality of capacitive elements for distributing any one of the reference values by capacitance ratio; and a control unit for switching a value compared to the analog value by the comparator from a reference value to a distribution value of the plurality of capacitive elements when the comparator outputs an m-bit digital value, wherein the analog value is converted to an  $(m + n)$  bit digital value.